A TinyOS driver for FRAM

ALL CONTRACTORS

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TinyOS driver (HIL) for FRAM

- FRAM is fast and power efficient --> suitable for WSN
- interface abstractions in TinyOS are designed for flash
- no erase/write block size resp. erase/write block size of 1
- undersized variables
- small adaptations necessary

Comparison with respect to	Type of Non-Volatile Memory	
	flash	FRAM
Available Interfaces	SPI/I2C/Parallel	SPI/I2C/Parallel
Sleep Mode Current	1 A	1 A
Data Retention	> 10 a	> 10 a
Write Cycles	$\sim 10^{5}$	$\sim 10^{10}$
Capacity ^a	≤ 32 Gibit (parallel) ≤ 128 Mibit (SPI)	$\leq 4 \text{ Mibit (parallel)} \\ \leq 2 \text{ Mibit (SPI)} \\ \leq 1 \text{ Mibit (I2C)} $
Energy Consumption ^b	90 nJ/bit	1.1 nJ/bit
Write Speed/byte ^c	~10 s	~ 400 ns

uint8_t --> uint32_t uint16_t --> uint32_t or

larger sector size but less flexible

Thank you!

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ALC: NO.